







Fig.6a.

Synchronous

Timing Phase=1 Byte

Payload LI-3 Bytes Narrow-band traffic 1 mini-cell per 125 μs

Timing byte is signed integer giving offset from NTR phase

Fig.6b.

Pleisochronous

Payload LI-I Bytes Narrow-band traffic 1 mini-cell per 125 μs

